

FRAUNHOFER INSTITUTE FOR PHOTONIC MICROSYSTEMS IPMS CENTER NANOELECTRONIC TECHNOLOGIES (CNT)



NEXT GENERATION FERROELECTRIC RAM



Fraunhofer Institute for Photonic Microsystems IPMS Center Nanoelectronic Technologies (CNT)

An der Bartlake 5 01109 Dresden Germany

Konrad Seidel +49 351 2607 3059 konrad.seidel@ipms.fraunhofer.de

www.ipms.fraunhofer.de

INTERCONNECTS

FERROELECTRIC RAM

Among the various emerging memory technologies the ferroelectric random access

memory (FRAM) is a promising candidate for future ultralow power nonvolatile memory

Memory group (EME) at Fraunhofer

IPMS business unit CNT investigates fully

CMOS compatible, hafnium oxide based

ferroelectrics. This lead free material system

enables the manufacturing of cost efficient

future embedded memories the key

disturb characteristic in memory arrays

need to be improved. For the investigation

a demonstrator has been built, utilizing a

64 bit hafnium oxide based FRAM with a

dedicated designed application-specific-

integrated-circuit (ASIC). The system is

accessed by an I²C interface.

retention, endurance and

To accomplish the requirements

applications. Therefore, he Emerging

and power saving CMOS chips.

parameters

HIGH-K DEVICES

NON-VOLATILE MEMORIES

ADVANTAGES

- CMOS compatible
- Non-volatile
- Lead free no PZT
- Scalable
- Fast read/write operation (switching speed in ns range)
- Low power & low voltage
- 10-year storage capability
- 300 mm industrial standard conditions
- Customer wafer processing possible
- Long-standing experience in process characterization and development
- Electrical and reliability test on wafer level
- ISO 9001 certified

of

APPLICATIONS

- Low power NVM / Sensor datalogging
- Embedded NVM
- Neuromorphic Computing
- Cost efficient CMOS & RFID-tags

